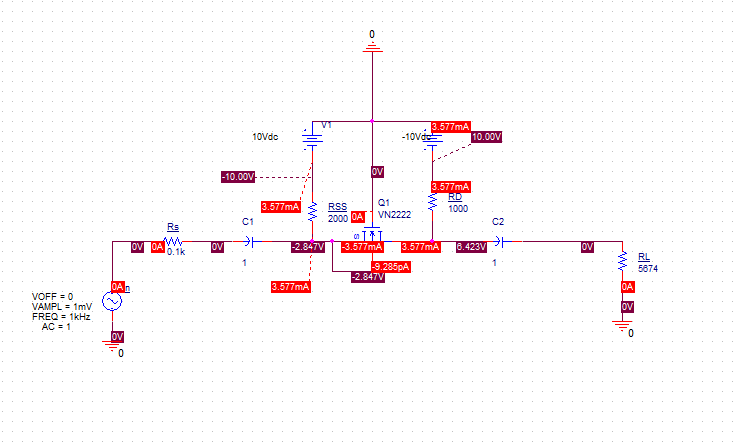
2.

a.

**DC Values**



b.

**Simulated DC Bias Parameters**

|  |  |
| --- | --- |
| DC Bias Parameters | |
| IDQ | 3.577mA |
| VGSQ | 2.847V |
| VDSQ | 6.423V |
| PD | 33.2mW |

c.

**Simulated Gain**



Vout peak is 6.2070mV

Vin peak = 0.9991mV

d.

**Simulated Input Resistance**

The calculated value was 100Ω. This is a 25% deviation from the actual value.

**Simulated Input Current**



The simulated input current is 7.4119µA.

**Simulated Input Voltage**



The simulated input voltage is 0.9991mV.

e.

**Simulated Output Resistance**

The calculated value was 5674Ω. This is a 0.23% deviation from the actual value.

**Simulated Output Voltage**



The simulated output voltage is 6.2070mV.

**Simulated Output Current**



The simulated output current is 1.0914µA.

f.

**Simulated Gain Over Temperature**



At 85°C

Vout peak is 6.4846mV

Vin peak = 0.9991mV

This is a 4% deviation from the original gain.

At 25°C

Vout peak is 6.2010mV

Vin peak = 0.9991mV

This is a 0.11% deviation from the original gain.

At -40°C

Vout peak is 5.9734mV

Vin peak = 0.9991mV

This is a 4% deviation from the original gain.

With a max 4% deviation from the original gain, the gain can be considered stable under changing temperature.